Rev 09/03 PATENT

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

h re application  $\cdot$ : Wushing Yin, et al.

Confirmation No. : 9143

FOR : INTEGRATED UNDERFILL PROCESS FOR BUMPED CHIP

ASSEMBLY

Attorney's Docket : INDUM-110XX

TC Art Unit: 3726

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on //-/2-2003.

Bv:

Victor B. Lebovici Registration No. 30,846

Attorney for Applicants

#### INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

It is desired to cite for the record in this application the enclosed references listed on the attached copy of PTO Form #1449. The paragraph(s) marked below are applicable to this Information Disclosure Statement.

[] (1) Pursuant to 37 C.F.R. § 1.97(b)(1) and (2), the attached Information Disclosure Statement is being filed within three months of the filing date of the above identified national application or within three months of the date of entry of the national stage as set forth in 37 C.F.R. § 1.491 of the above identified application. Accordingly, applicant(s) believes that no fee or statement under 37 C.F.R. § 1.97(e) is required.

Filed: August 7, 2003 TC Art Unit: 3726

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[X] (2) Pursuant to 37 C.F.R. § 1.97(b)(3), applicant(s) believes the attached Information Disclosure Statement is being filed before the mailing date of a first Office action on the merits. Accordingly, applicant(s) believes that no fee or statement under 37 C.F.R. § 1.97(e) is required.

- [ ] (3) Pursuant to 37 C.F.R. § 1.97(b)(4), applicant(s) believes the attached Information Disclosure Statement is being filed before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Accordingly, applicant(s) believes that no fee or statement under 37 C.F.R. § 1.97(e) is required.
- [] (4) Pursuant to 37 C.F.R. § 1.97(c), the attached Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by:
  - [ ] a statement under 37 CFR § 1.97(e); or
  - [ ] the fee set forth in § 1.17(p).

# PETITION UNDER 37 CFR § 1.97(d)

[ ] (5) Pursuant to 37 CFR § 1.97(d), applicant(s) hereby petitions the Commissioner to consider the attached Information Disclosure Statement which is being filed on or before payment of the issue fee. This petition is accompanied by a statement under 37 C.F.R. § 1.97(e) and the petition fee set forth in 37 C.F.R. § 1.17(p).

### STATEMENT UNDER 37 C.F.R. § 1.97(e)(1)

[] (6) The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.

### STATEMENT UNDER 37 C.F.R. § 1.97(e)(2)

[ ] (7) The undersigned hereby states that no item of information contained in the attached Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to the knowledge of the

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undersigned after making reasonable inquiry, no item of information contained in the attached Information Disclosure Statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

The filing of this Information Disclosure Statement is not a representation by the undersigned as to personal knowledge of the contents of every word or phrase of the material enclosed or that reliance on other suitably trained professionals has not been made.

If a search report of a searching agency is enclosed identifying the nature of the relevance of each document, such a designation is deemed to satisfy 37 C.F.R. § 1.98(a)(3) even if in a foreign language because the codes are the same in all languages. However, applicant(s) does not necessarily adopt the position reflected by that report.

The Commissioner is hereby authorized to charge payment of any additional fees associated with this communication or credit any overpayment to Deposit Account No. 23-0804. Triplicate copies of this letter are enclosed.

Respectfully submitted,

WUSHING YIN, ET AL.

Victor B. Lebovici

Registration No. 30,864

Attorney for Applicants

WEINGARTEN, SCHURGIN,
GAGNEBIN & LEBOVICI LLP
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Boston, Massachusetts 02109

Telephone: (617) 542-2290 Telecopier: (617) 451-0313

Enclosure 298205 Rev (8/00) 1 4 2003 E

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application

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Application No.

10/636,105

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For

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**ASSEMBLY** 

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INDUM-110XX

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### STATEMENT UNDER 37 C.F.R. § 1.97(e)(1)

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Respectfully submitted,

WUSHING YIN, ET AL.

Victor B. Lebovici

Registration No. 30,864 Attorney for Applicants

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Enclosure 298205



Date: November 12, 2003 Page 1 of 2

U.S. DEPARTMENT OF COMMERCE					ATTY. DOCK	ET NO.	APPLICATION NO.			
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EXAMINER	DOCUMENT NUMBER	PUBLICATION/	/ N/		ME	CLASS	SUBCLASS	FILING	DATE	
INITIAL		ISSUE DATE								
	US2003/0096452	5/22/2003	Yin, et al.	in, et al.		438	108			
	US2003/0096453	5/22/2003	Wang, et	Wang, et al.			108			
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	WO 99/56312	11/4/1999	WIPO				Х			
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	OTHER DOCU	MENTS (including Au	thor, Title, D	Date	e, Pertinent Pag	ges, etc.)				
	Product Data Sheet; "Epoxy Flip Chip Flux PK-001"; Indium Corporation of America									
	Product Data Sheet; "Pb-Free Epoxy Flip Chip Flux PK-002"; Indium Corporation of America									
	Surface Mount Technology; Materials, Processes and Equipment; Carmen Capillo; Unisys Corporation; San Jose, CA									
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	"No Flow Underfill Reliability is Here - Finally?"; Michael A. Previti; Cookson Semiconductor Packaging Materials; Alpharetta, GA										
	"The Development of No-Flow Underfill Materials for Flip-Chip Applications"; Dr. C. P. Wong; S. H. Shi; School of Materials Science and Engineering & Packaging Research Center; Georgia Institute of Technology; February 2, 1999										
	"No-Flow Underfill Materials for Environment Sensitive Flip-Chip Process; A Dissertation Presented to The Academic Faculty; Zhuqing Zhang; Georgia Institute for Technology; June 2001Chi Flfor										
	"A Novel Approach to Incorporate Silica Filler into No-Flow Underfill"; Zhuqing Zhang, Jicum Lu; C. P. Wong; School of Materials Science and Engineering and Packaging Research Center; Georgia Institute of Technology; Georgia; Pages 1-7										
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